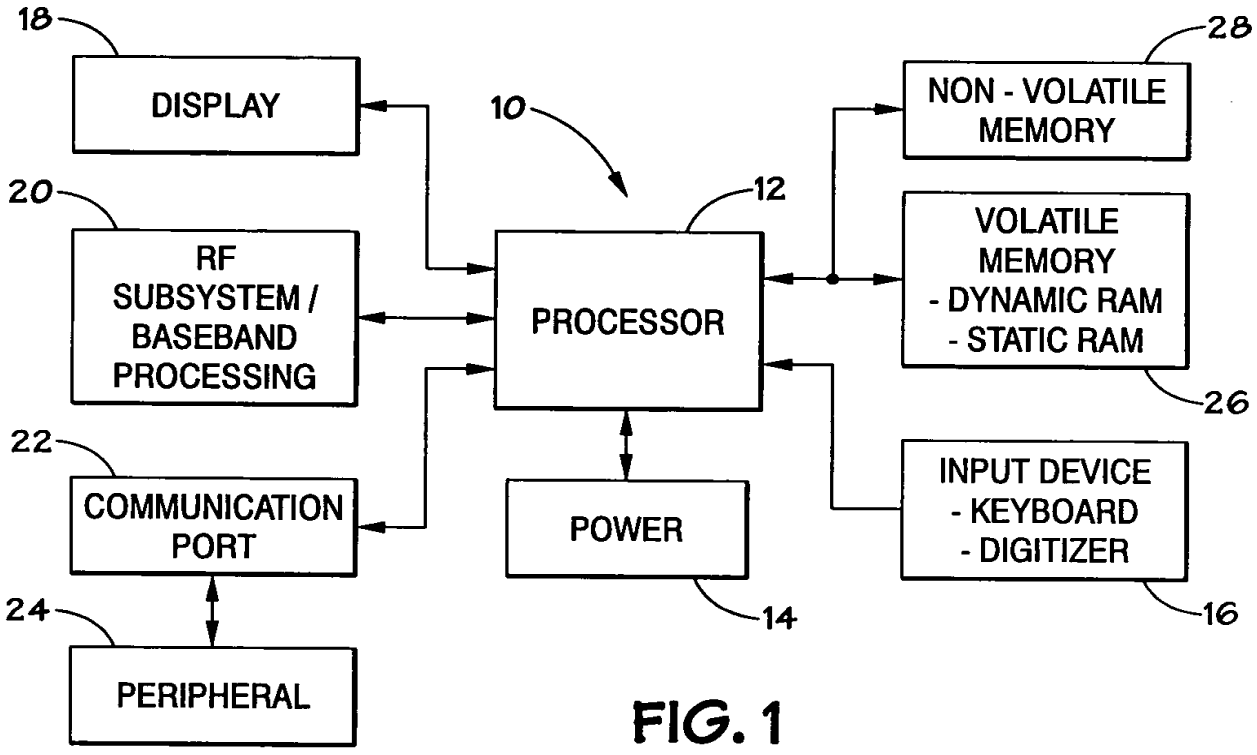

18
DISPLAY
10
28
NON - VOLATILE MEMORY
20
RF SUBSYSTEM / BASEBAND PROCESSING
12
PROCESSOR
VOLATILE MEMORY - DYNAMIC RAM - STATIC RAM
26
22
COMMUNICATION PORT
POWER
INPUT DEVICE - KEYBOARD - DIGITIZER
24
PERIPHERAL
14
16

FIG. 1

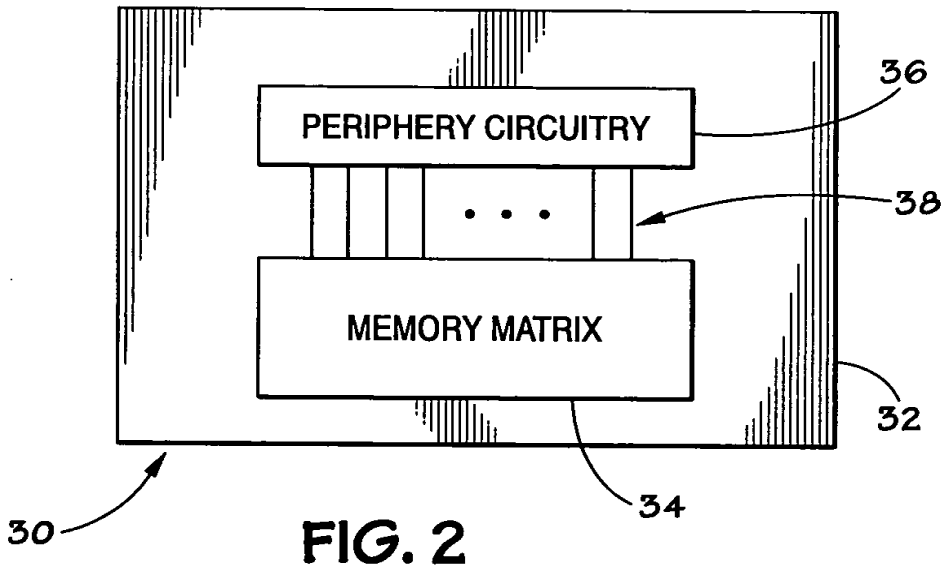

PERIPHERY CIRCUITRY
36
38
MEMORY MATRIX
32
30
34

FIG. 2

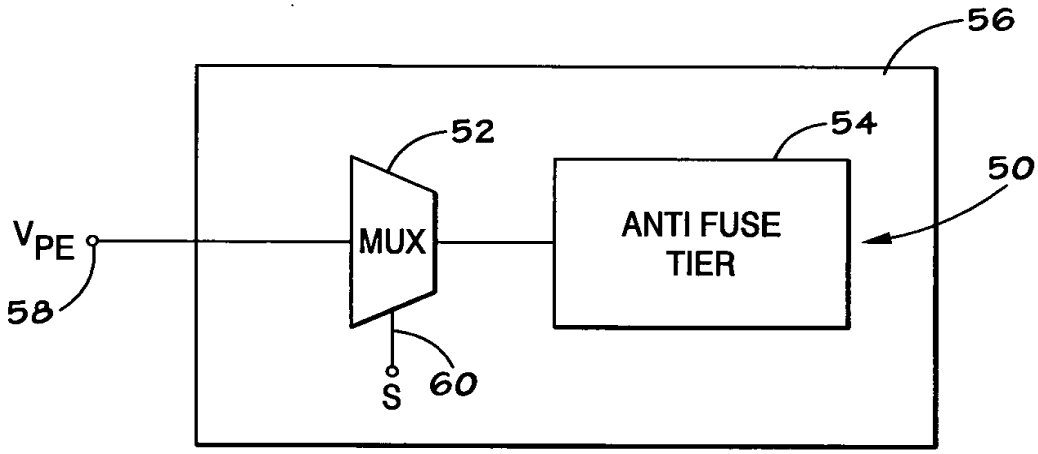

56
52
54
50
MUX
ANTI FUSE
TIER
V_{PE}
58
S
60

FIG. 3

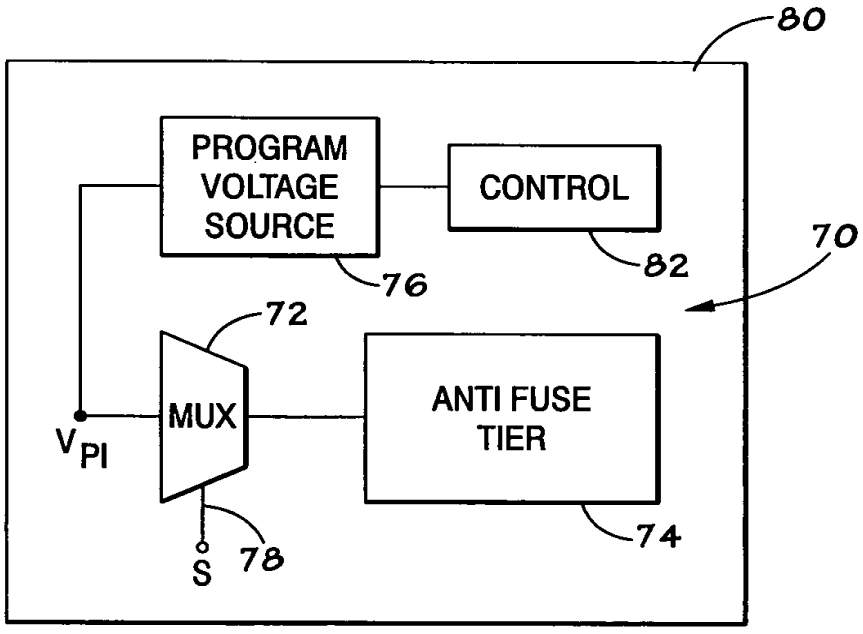

80
PROGRAM
VOLTAGE
SOURCE
CONTROL
82
70
76
72
MUX
ANTI FUSE
TIER
V_{PI}
74
S
78

FIG. 4

FIG. 5

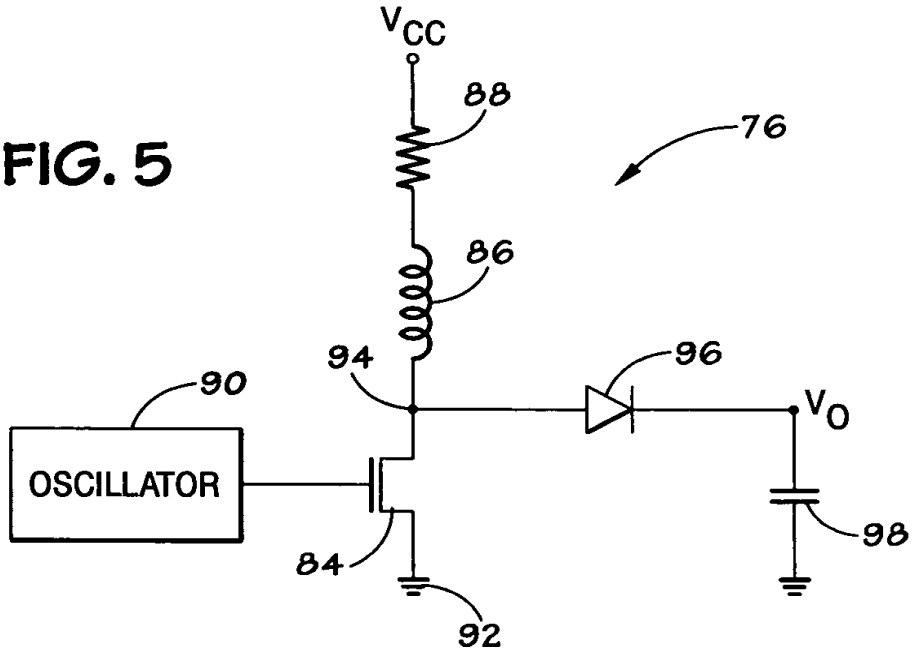

V_{CC}
88
76
86
94
96
90
V_O
OSCILLATOR
84
98
92

FIG. 6

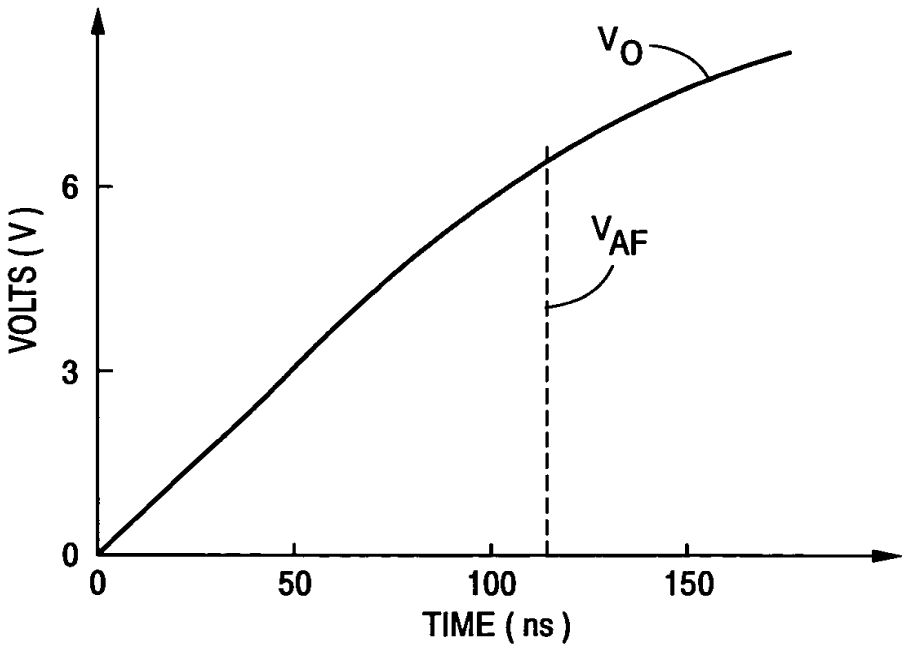

V_O
6
V_{AF}
VOLTS (V)
3
0
0
50
100
150
TIME (ns)

FIG. 7

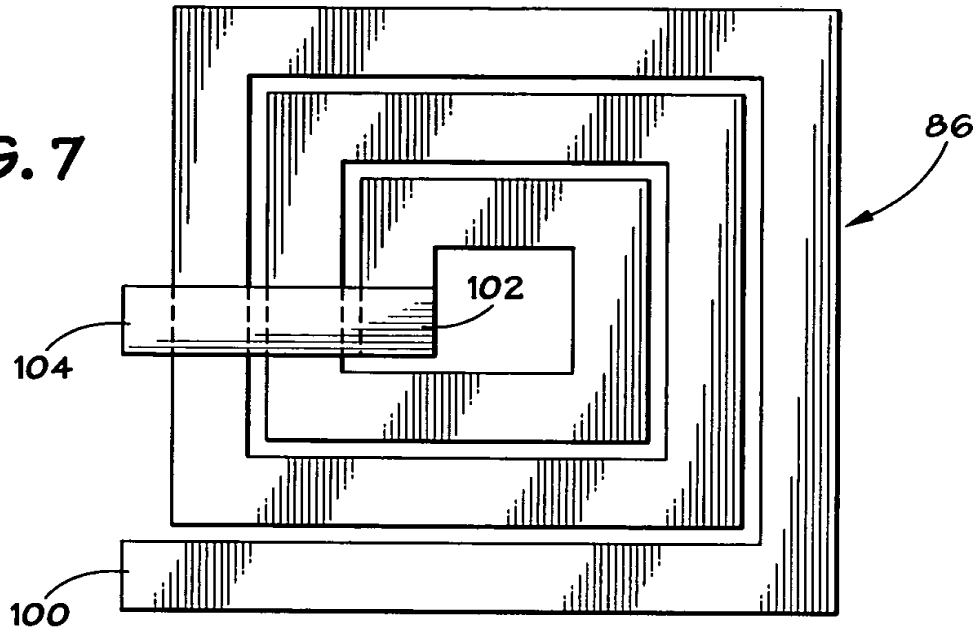


FIG. 8A

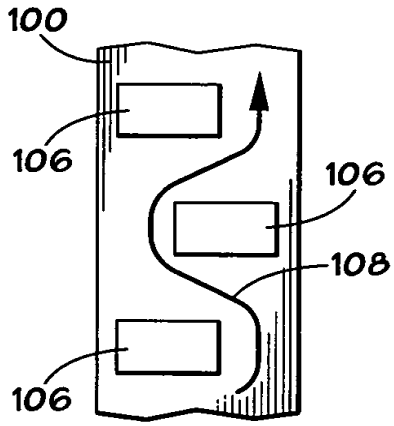
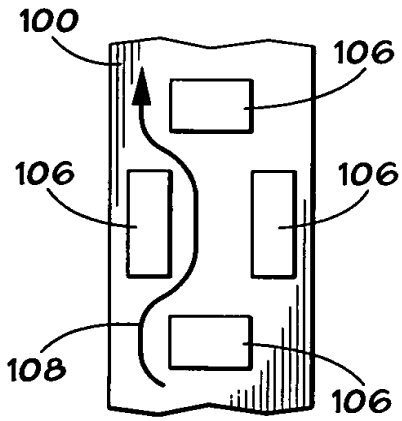


FIG. 8B



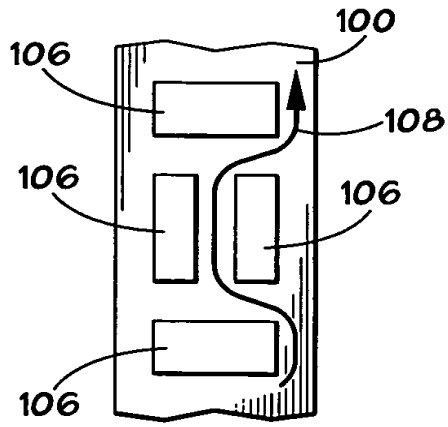


FIG. 8C